FIG.1A

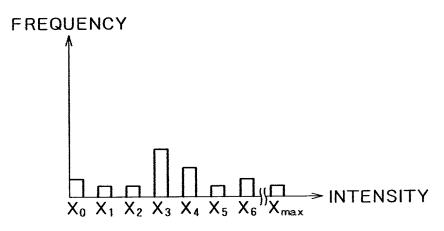


FIG.1B

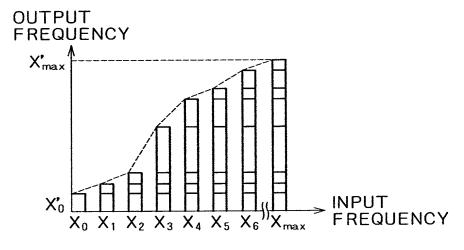
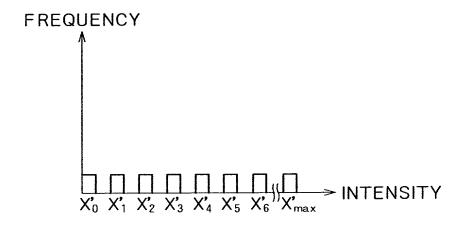


FIG.1C



MANIPULATING SECTION IMAGE PROCESSING SECTION CONTROLLING SECTION A/D CONVERTER 12 10 AUTOMATIC GAIN CONTROLLER TIMING GENERATOR $\omega \sim$ CDS ∞~ CCD ഗ

<u>7</u>

DRIVE

14

<u>~</u>

9

7

F1G.2

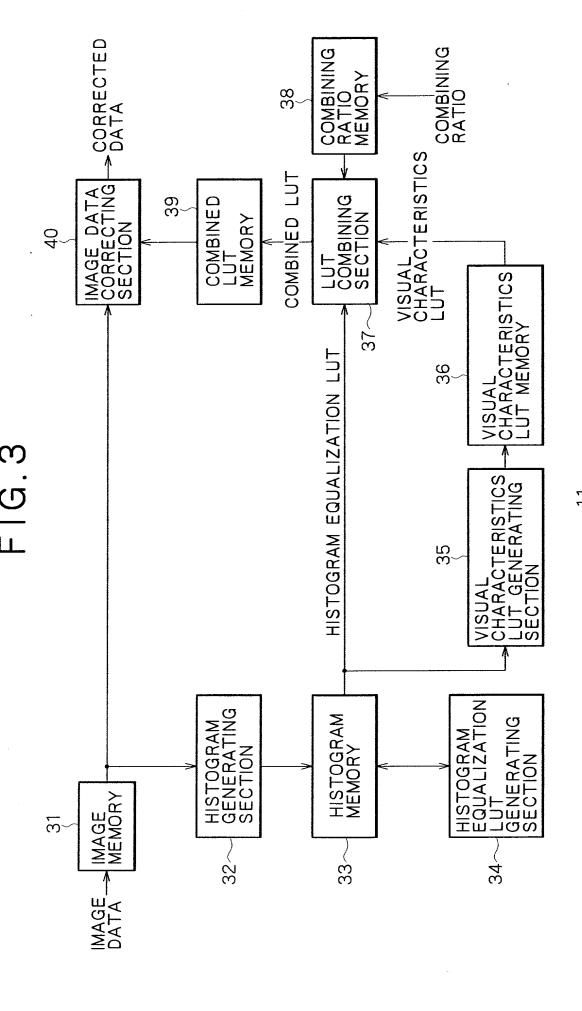
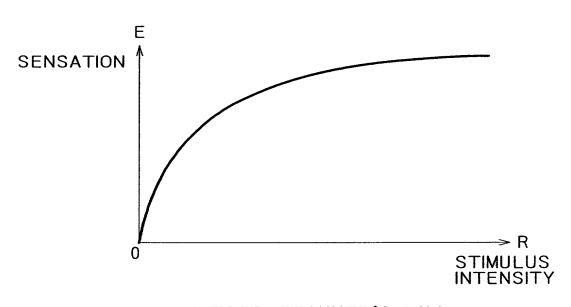


FIG.4



WEBER-FECHNER'S LAW

FIG.5

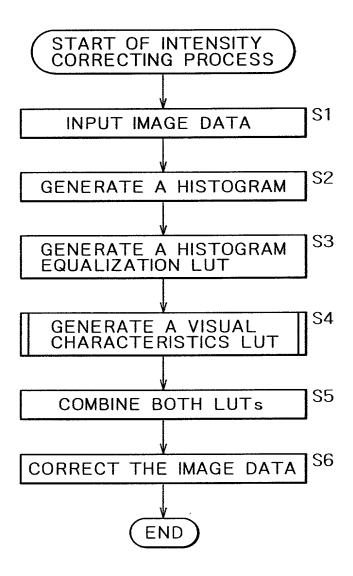


FIG.6

